SSC8V7N65GT8

N-Channel Enhancement Mode Power MOSFET

➢ Features

V_{DS}	V_{GS}	$R_{DS(ON)}$ Typ.	I_D
650V	±30V	1.15Ω@10V	7A

➢ Description

- This device is N-Channel enhancement MOSFET.
- Fast Switching.
- Improved dv/dt Capability.

100% UIS + ΔVDS + Rg Tested!

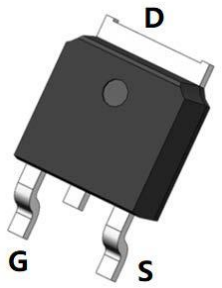
➢ Applications

- Load Switch
- PWM Application
- Power Management

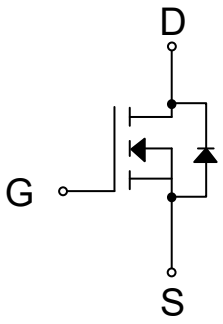
➢ Ordering Information

Device	Package	Shipping
SSC8V7N65GT8	TO252	2500/Reel

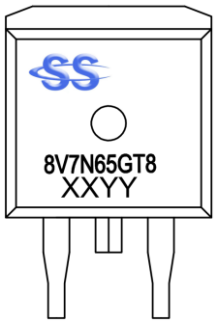
➢ Pin Configuration



TO252 (Top View)



Pin Configuration



Marking

(XXYY: Internal Traceability Code)

➢ Absolute Maximum Ratings (T_J=25℃ unless otherwise noted)

Symbol	Parameter		Ratings	Unit
V_{DSS}	Drain-to-Source Voltage		650	V
V_{GSS}	Gate-to-Source Voltage		±30	V
I_D	Continuous Drain Current	T_J=25℃	7	A
		T_J=100℃	4	
I_{DM}	Pulsed Drain Current [a]		28	A
E_{AS}	Single Pulsed Avalanche Energy		245	mJ
P_D	Power Dissipation, T_J=25℃		69	W
T_{STG} /T_J	Junction & Storage Temperature Range		-55 to 150	℃

➢ Thermal Resistance Ratings (T_J=25℃ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient[b]	60	℃/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.8	

Note:

a. Repetitive Rating: Pulsed width limited by maximum junction temperature.

b. $R_{\theta JA}$ is measured with the device mounted on a minimum recommended pad of 2oz copper FR4 PCB.

➢ Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	650			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 650V, V_{GS} = 0V			1.0	μA
Gate-Source Leak Current	I_{GSS}	V_{GS} = ±30V, V_{DS} = 0V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250uA	2	3	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = 10V, I_D = 3.5A		1.15	1.35	Ω
Input Capacitance	C_{ISS}	V_{DS} = 25V, V_{GS} = 0V, f = 1MHz		1089		pF
Output Capacitance	C_{OSS}			100		
Reverse Transfer Capacitance	C_{RSS}			14		
Total Gate Charge	Q_G	V_{GS} = 0 to 10V, V_{DS} = 520V, I_D = 7A		27		nC
Gate to Source Charge	Q_{GS}			6		
Gate to Drain Charge	Q_{GD}			11		
Turn-on Delay Time	$T_{D(ON)}$	V_{GS} = 10V, V_{DS} = 319V, I_D = 7A, R_G = 24Ω		19		ns
Rise Time	T_r			29		
Turn-off Delay Time	$T_{D(OFF)}$			78		
Fall Time	T_f			35		
Maximu Continuous Drain to Source Diode Forward Current	I_S				7	A
Maximum Pulsed Drain to Source Diode Forward Current	I_{SM}				28	A
Drain to Source Diode Forward Voltage	V_{SD}	VGS = 0V, IS = 7A			1.2	V
Body Diode Reverse Recovery Time	Trr	IF = 7A, di/dt = 100A/us		340		ns
Body Diode Reverse Recovery Charge	Qrr			2.9		μC

➢ Typical Performance Characteristics (T_J=25℃ unless otherwise noted)

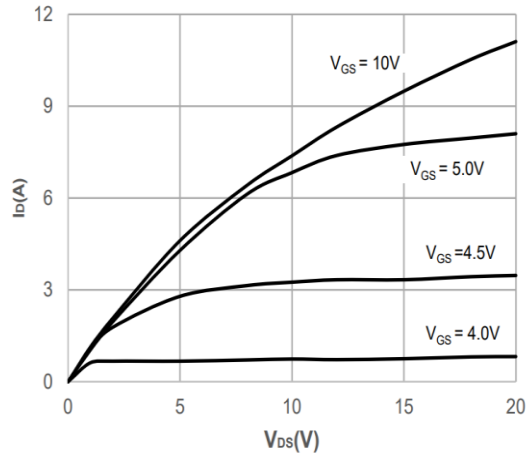


Figure 1: Output Characteristics

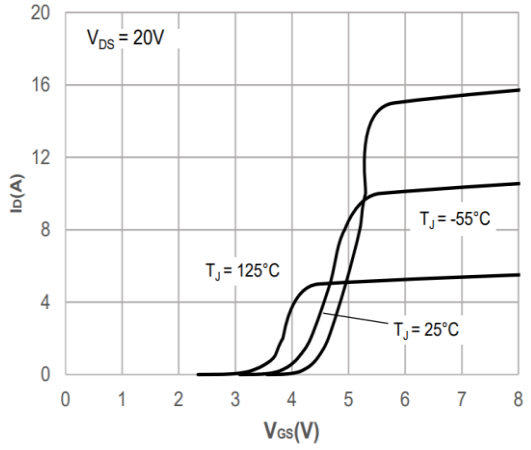


Figure 2: Typical Transfer Characteristics

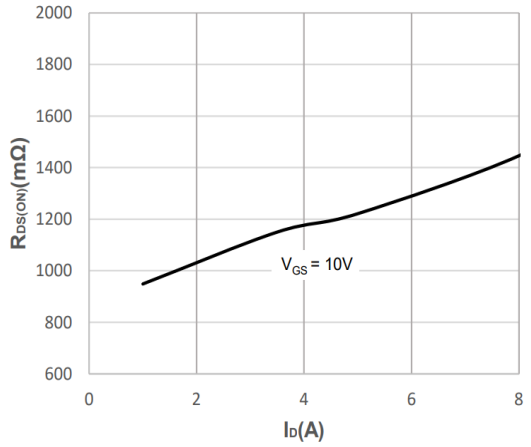


Figure 3: On-resistance vs. Drain Current

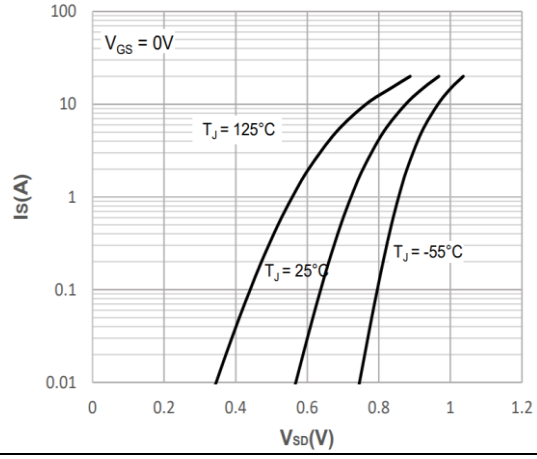


Figure 4: Body Diode Characteristics

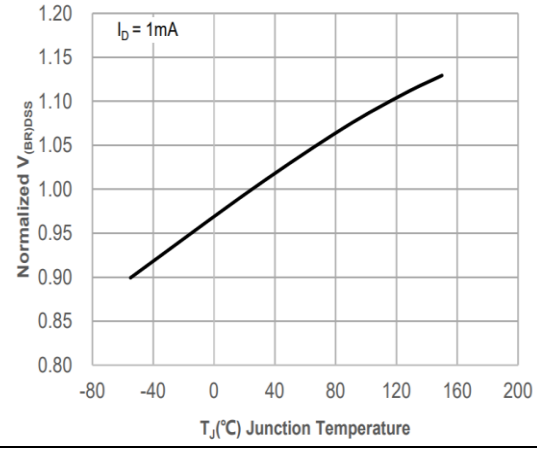


Figure 5: Normalized Breakdown voltage vs. Junction Temperature

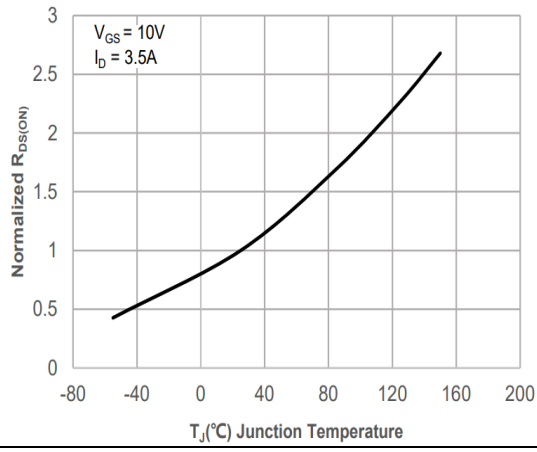


Figure 6: Normalized on Resistance vs. Junction Temperature

➢ Typical Performance Characteristics (T_J=25℃ unless otherwise noted)

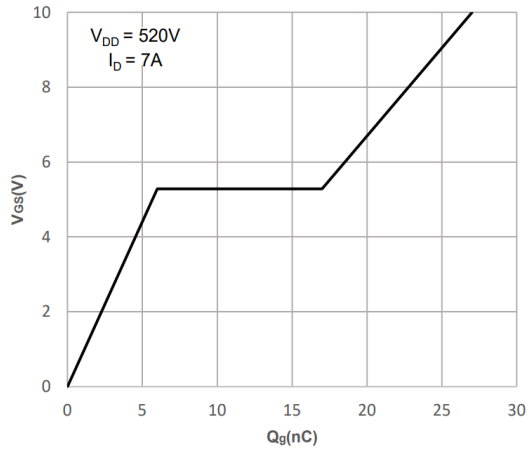


Figure 7: Gate Charge Characteristics

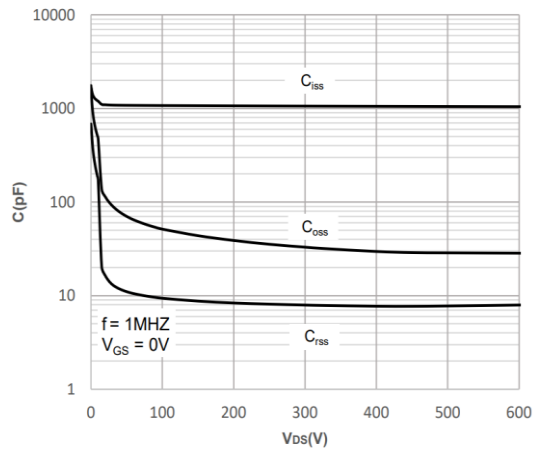


Figure 8: Capacitance Characteristics

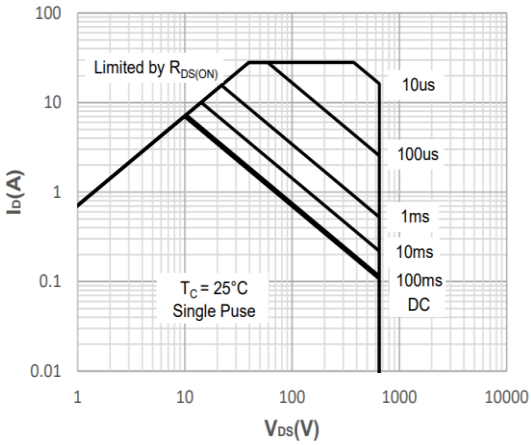


Figure 9: Maximum Safe Operating Area

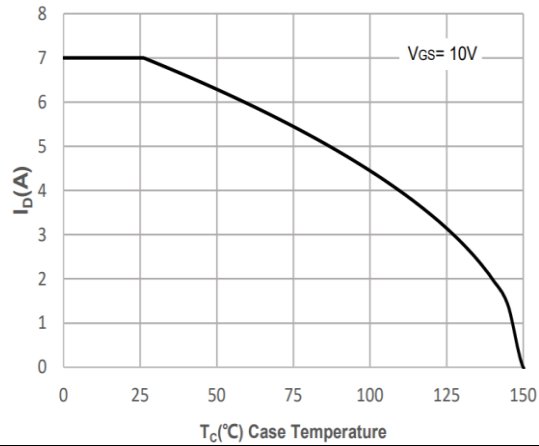


Figure 10: Maximum Continuous ID vs. Case Temperature

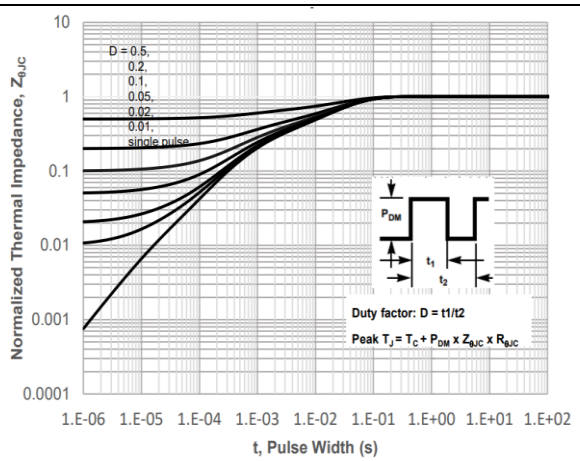


Figure 11: Normalized Maximum Transient Thermal Impedance

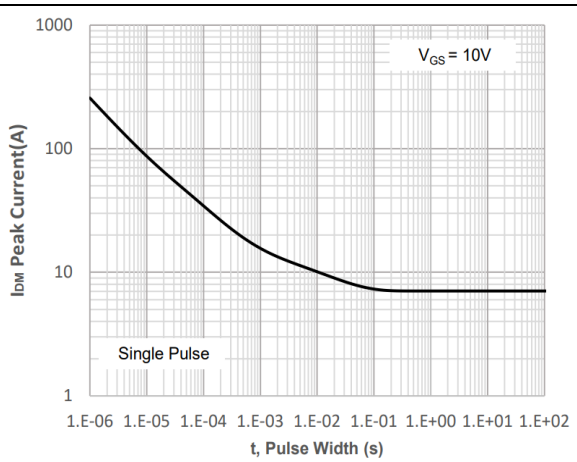
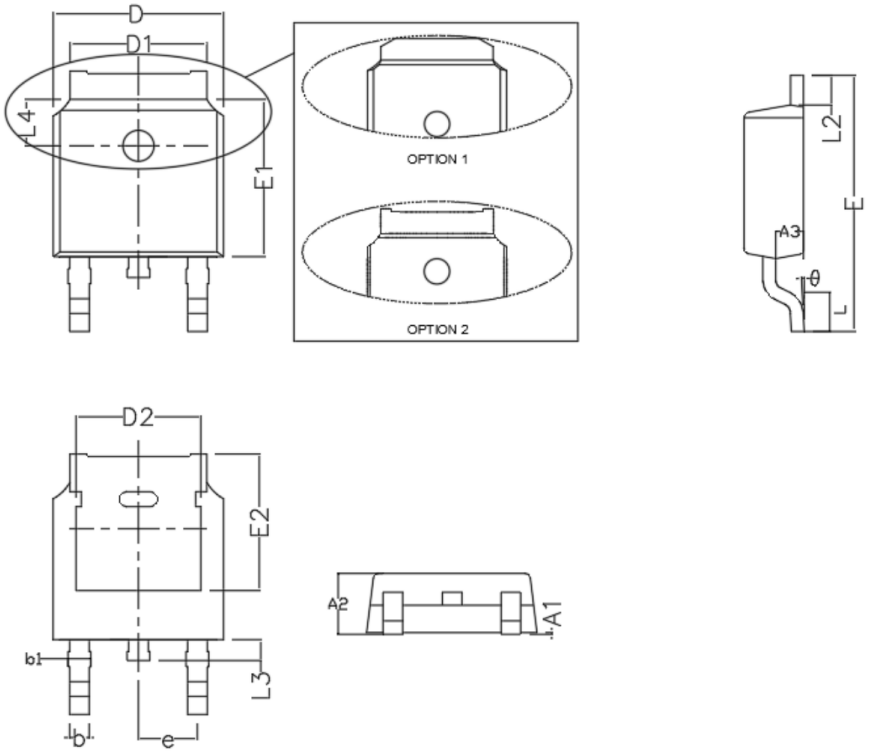


Figure 12: Peak Current Capacity

➢ Package Information

TO252



Symbol	MILL IMETER			Symbol	MILL IMETER		
	Min	Nom	Max		Min	Nom	Max
A1	0.000	/	0.200	E1	5.900	6.100	6.300
A2	2.100	2.300	2.400	E2	5.100	5.450	5.600
A3	0.900	1.040	1.170	e	2.286TYP		
b	0.635	0.762	0.910	L	1.270	1.500	2.032
b1	0.680	0.840	1.145	L2	0.900	1.100	1.270
D	6.350	6.600	6.800	L3	0.600	0.800	1.000
D1	4.950	5.330	5.500	L4	1.600	1.800	2.000
D2	4.315	4.830	5.230	θ	0°	/	10°
E	9.395	10.100	10.500				

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